Attorney Docket Number	DIGI8					
First Named Inventor	Duchesne					
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Application Number						
Filing Date						
Group Art Unit						
Examiner Name						
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As a below named inventor, I hereby declare that:												
My residence, post office address, and citizenship are as stated below next to my name												
I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:												
RAID SYSTEM HAVING CHANNEL CAPACITY UNAFFECTED BY ANY SINGLE COMPONENT FAILURE												
the specification of which (Title of the Invention) is attached hereto OR												
was filed on (MM/DD/YYYY) as United States Application Number or PCT International Application Number and was amended on (MM/DD/YYYY) (if applicable)												
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e identified specification, including the claims, as												
ity as defined in 37 CFR 1.56												
I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT international application having a filing date before that of the application on which priority is claimed												
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[Page 1 of 2]
Burden Hour Statement: This form is estimated to take 0.4 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

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DECLARATION

I hereby claim the benefit under Title 35, United States Code §120 of any United States application(s), or §365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the

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DECLARATION

ADDITIONAL INVENTOR(S) Supplemental Sheet

Name of Additional Joint Inventor, if any:							A	pet	tition has b	peen filed fo	or this ur	nsigne	ed invento	r			
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[0010] U.S. Pat. No. 6,073,218 discloses an apparatus for coordinating multiple RAID storage array controllers' access to a single array of storage devices. Each of a number of storage array controllers process different I/O requests on an array of common shared storage devices.

One storage array controller is designated primary with respect to the storage devices.

Concurrent access to the storage devices is coordinated by the storage array controllers.

[0011] None of the prior art RAID systems achieves the advantages of the present invention, that of preserving the channel capacity or speed of the system in the face of failure of a connector, storage unit, or storage array storage array controller, all with minimal redundancy of components and minimal cost. In particular, in this invention a single passive storage array controller is available to replace any one of two or more active storage array controllers when an active storage array controller becomes defective.

BRIEF SUMMARY OF THE INVENTION.

[0012] This invention is a RAID system which is able to function with undiminished channel capacity or speed despite the failure of any one component, with minimal redundancy of components. The system comprises n active storage array controllers, n arrays of storage units, each active storage array controller connected by at least one connector to one array of storage units, each active storage array controller controlling one or more arrays of storage units, and one only passive storage array controller. The passive storage array controller is connected by two connectors to each active storage array controller, and is able to control any one of the arrays of storage devices. In normal operation, each active storage array controller controls one array of storage devices. In the event of failure of any of the active storage array controllers, the passive storage array controller assumes the identity of the failed storage array controller and assumes